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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	09/945,553
Filing Date	August 30, 2001
First Named Inventor	Gonzalez, Fernando
Group Art Unit	2823
Examiner Name	Fourson, George

Sheet 1 of 1

Attorney Docket No: 303.775US1

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Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
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Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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/George Fourson/

DATE CONSIDERED

07/09/2007

Substitute Disclosure Statement Form (PTO-1449)

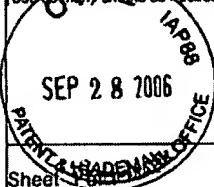
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

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/GF/		WOLF, S., <u>Silicon Processing for the VLSI Era, (1), Process Technology, (1986), 514-517 and 551-552</u>	

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